

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

)
Zhongmin Li, et al.))
10/637,840)) Art Unit) 2819
August 8, 2004)
4352)
DIGITALLY CONTROLLED IMPEDANCE DRIVER MATCHING FOR WIDE VOLTAGE SWINGS AT INPUT/OUTPUT NODE AND HAVING PROGRAMMABLE STEP SIZE)))
Vibol Tan))
022913))
	10/637,840 August 8, 2004 4352 DIGITALLY CONTROLLED IMPEDANCE DRIVER MATCHING FOR WIDE VOLTAGE SWINGS AT INPUT/OUTPUT NODE AND HAVING PROGRAMMABLE STEP SIZE Vibol Tan

CERTIFICATE OF DEPOSIT UNDER 37 C.F.R. § 1.8

I hereby certify that following documents are being deposited with the United States Postal Service as Express Mail (EV 661144954 US), postage prepaid, in an envelope addressed to: Box: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 5th day of April 2005.

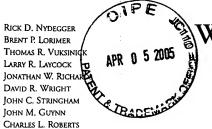
- Transmittal of Issue Fee and Supplemental Declaration (3 pages) (in duplicate)
- PTO Form PTOL-85B transmitting payment of Issue Fee
- PTO 2038 Credit Card form for \$1,445.00
- Postcard

Respectfully submitted,

Adrian J. Lee

Attorney for Applicant Registration No. 42,785

Customer No. 022913



WORKMAN | NYDEGGER

INTELLECTUAL PROPERTY ATTORNEYS

1000 Eagle Gate Tower 60 East South Temple Salt Lake City, Utah 84111 Telephone: (801) 533-9800

FAX: (801) 328-1707 Website: Http://www.wnlaw.com

Express Mailing Label No.: EV 661144954 US

PATENT APPLICATION
Docket No. 14993.31

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of)
	Zhongmin Li, et al.)
Serial No.:	10/637,840)) Art Unit) 2819
Filed:	August 8, 2004)
Conf. No.:	4352)
For:	DIGITALLY CONTROLLED IMPEDANCE DRIVER MATCHING FOR WIDE VOLTAGE SWINGS AT INPUT/OUTPUT NODE AND HAVING PROGRAMMABLE STEP SIZE))))
Examiner:	Vibol Tan)
Customer No :	022913)

TRANSMITTAL OF ISSUE FEE PAYMENT

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowance and Issue Fee Due dated February 4, 2005, please find enclosed herewith the following items for filing in the United States Patent and Trademark Office in connection with the above identified patent application:

X Issue Fee Transmittal (PTOL-85B) submitted pursuant to 37 C.F.R. § 1.311.

W

ij

Dana L. Tangren Eric L. Maschoff Charles J. Veverka

ROBYN L. PHILLIPS
RICHARD C. GILMORE *

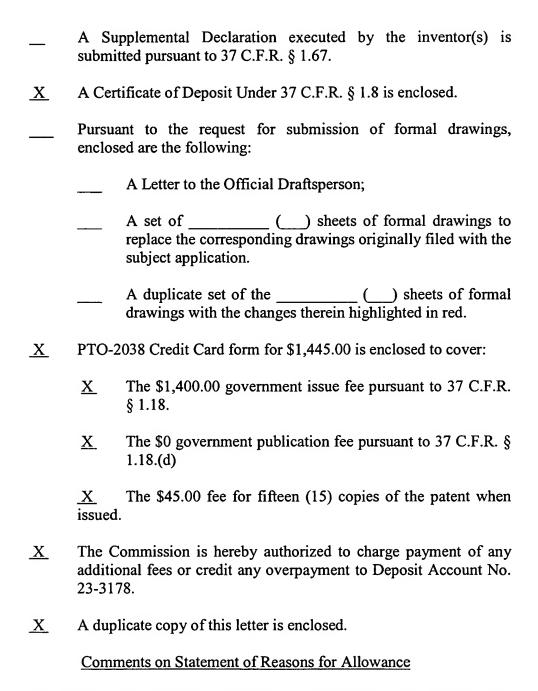
Sterling A. Brennan R. Burns Israelsen David R. Todd

DAVID B. DELLENBACH L. DAVID GRIFFIN ADRIAN J. LEE FRASER D. ROY CARL T. REED

R. PARRISH FREEMAN, IR. PETER F. MALEN, JR. L. REX SEARS, Ph.D. WILLIAM R. RICHTER ERIC M. KAMERATH ROBERT E. AYCOCK JENS C. JENKINS KEVIN W. STINGER William J. Athay MICHAEL B. DODD RYAN D. BENSON SARA D. IONES TIGE KELLER JANNA L. JENSEN MATTHEW D. TODD J. LaVar Oldham MICHAEL J. FRODSHAM JOSEPH L. KRUPA BRETT A. HANSEN BRETT I. JOHNSON MATTHEW A. BARLOW WESLEY C. ROSANDER Andrew S. Hansen CHAD E. NYDEGGER Joseph G. Pia CLINTON E. DUKE RYAN N. FARR * IAMES B. BELSHE KIRK R. HARRIS KEELY SCHNEITER MICHAEL M. BALLARD DAVID A. IONES Shane K. Jensen JONATHAN M. BENNS, PH.D.

VERNON R. RICE §
OF COUNSEL

Admitted only in California
 Admitted only in Virginia



On page 2 of the Examiner's Notice of Allowability, the Examiner has indicated that no prior art was found that "applicants have canceled rejected claims 1-6,9,15,17,21,23,24,28,29,33 and 34; and amended claims 7,8,10-14,16,18-20,22,25-27,30-32 and 35-40, in order to place this instant application in

condition for allowance. In combination with other limitations of the claims, the cited prior arts fail to teach the third resistor having a third ideal resistance that is ninety to one hundred and ten percent of the ideal resistance, as recited in amended claims 7,18 and 30; the cited prior arts also fail to teach a noise attenuation circuit, as claimed in claims 25,35 and 38."

Applicants respectfully submit that the claimed invention as set forth in each of the independent claims and the dependent claims must be read as a whole, and not as a single feature or subcombination of features which represent less than the entirety of the claimed invention as a whole. While a particular feature or subcombination of features referred to by the Examiner in the Statement of Reasons for Allowance may represent a basis for distinguishing the claimed invention over the prior art, Applicants further submit that this may not necessarily be the *sole* ground for distinguishing the claimed invention over the prior art of record. Accordingly, the Examiner's statement should, in Applicants' view, not be read as constituting or meaning that the invention can or should be reduced to a single "feature" of the invention or to a subcombination of features that is less than the entire invention claimed as a whole, nor that the single feature referenced by the Examiner or subcombination of features referenced by the Examiner in the Statement of Reasons for Allowance is the only or sole grounds for distinguishing the invention over the prior art of record.

Please address all future correspondence in connection with the aboveidentified patent application to the attention of the undersigned.

Dated this 4th day of April 2005.

Respectfully submitted,

Adrian J. lee

Attorney for Applicant Registration No. 42,785

Customer No. 022913

AJL:ds DS0000003312V001